

FIG.1

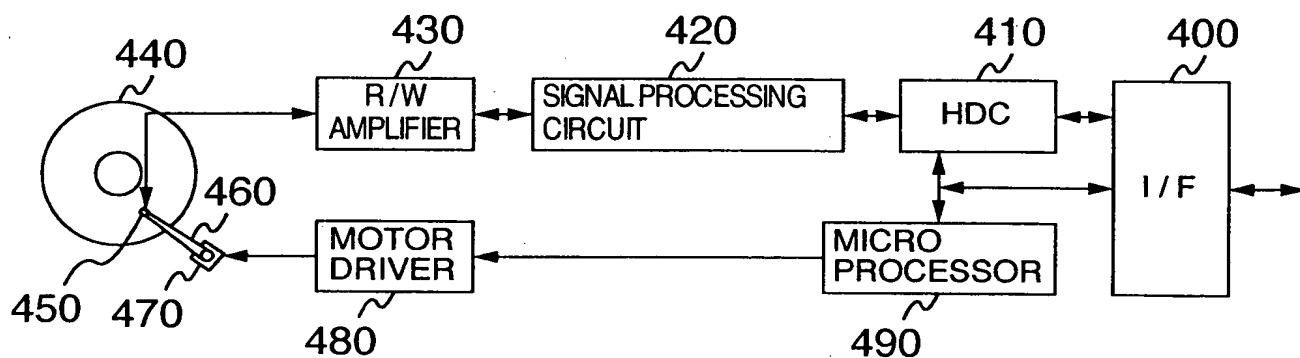


FIG.3

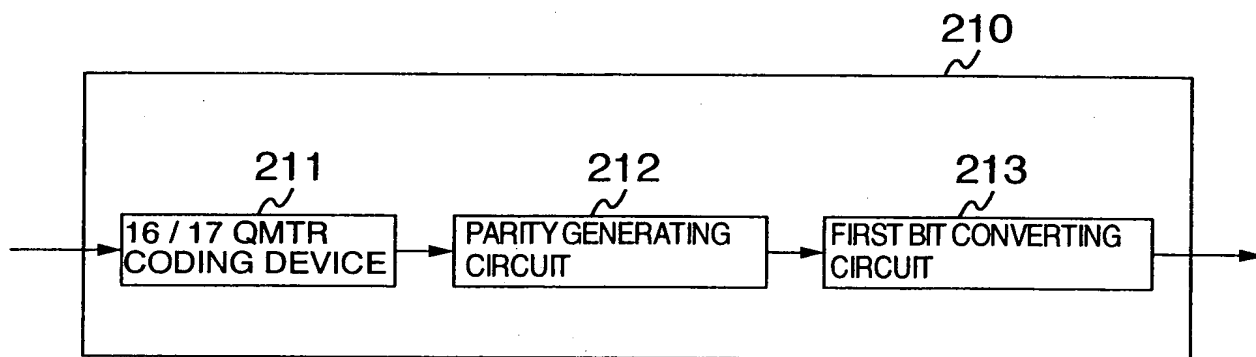


FIG.2

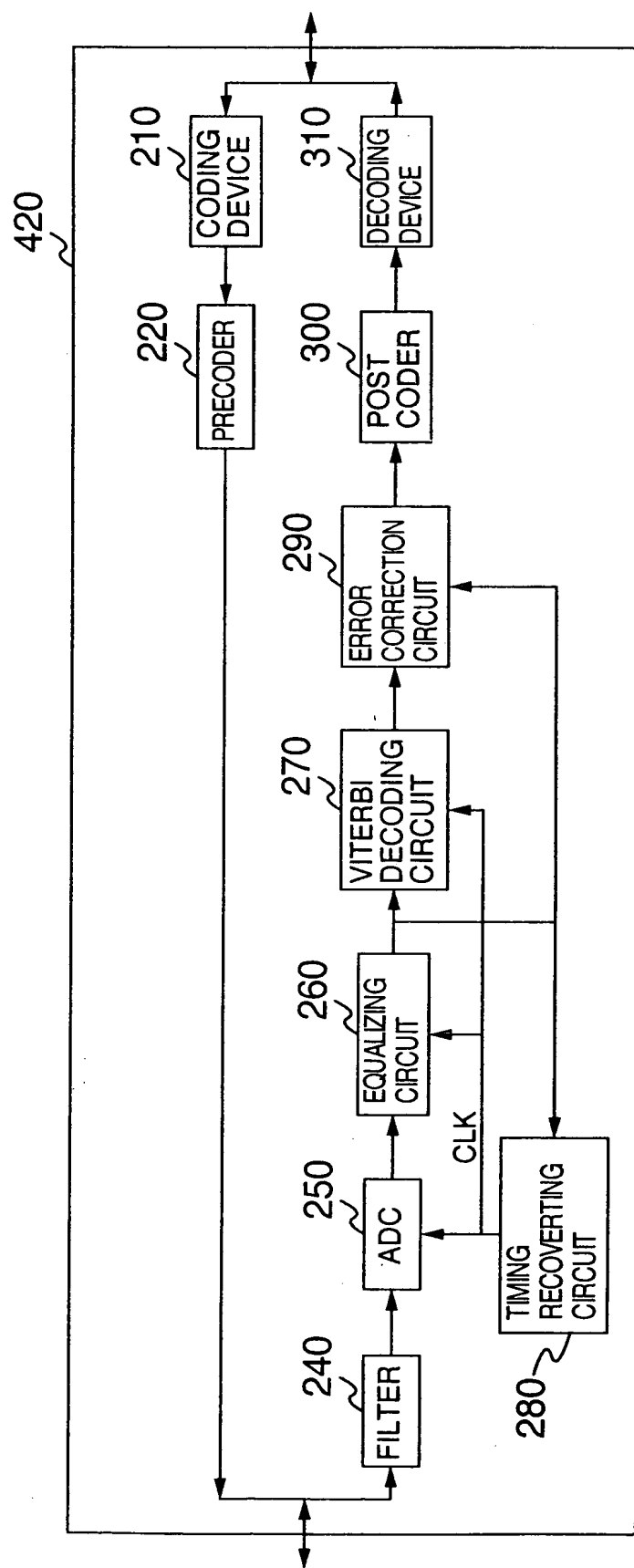


FIG.4

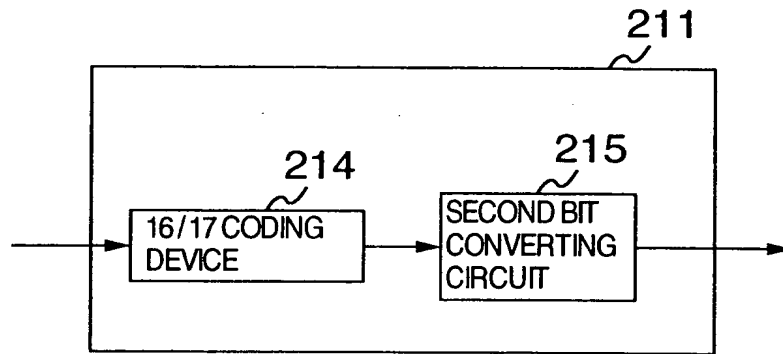


FIG.5

INPUT			OUTPUT		
PREV_LSB	PARITY	CURR_MSE	PREV_LSB	PARITY	CURR_MSE
0001	1	1100	0001	0	1110
1001	1	1100	1001	0	1110
0101	1	1100	0101	0	1110
1101	1	1100	0111	0	1100
0001	1	1101	0010	0	1110
1001	1	1101	0110	0	1110
0101	1	1101	0111	0	0101
1101	1	1101	1101	0	1110
0011	1	1000	0111	0	1000
1011	1	1000	0111	0	0100
0011	1	1001	0111	0	1001
1011	1	1001	0111	0	0110
0011	1	1010	0111	0	1010
1011	1	1010	1010	0	1110
0011	1	1011	0011	0	0111
1011	1	1011	0111	0	1011
0011	1	1100	0111	0	0010
1011	1	1100	1011	0	1110
0011	1	1101	0111	0	1101
1011	1	1101	0100	0	1110

FIG.6

INPUT			OUTPUT		
PREV_LSB	PARITY	CURR_MSB	PREV_LSB	PARITY	CURR_MSB
000000	0	000000	000111	0	111000
000000	0	000001	000111	0	111001
000000	0	000010	000111	0	111010
000000	0	000011	000111	0	111011
100000	0	000000	100111	0	111000
010000	0	000000	010111	0	111000
110000	0	000000	110111	0	111000
1000	0	000000	1000	0	111000
0100	0	000000	0111	0	001100
1100	0	000000	0111	0	001110
0010	0	000000	0111	0	000010
1010	0	000000	1000	0	111010
0110	0	000000	0111	0	000000
1110	0	000000	1110	0	111000
000000	0	0001	000111	0	0001
000000	0	0010	001100	0	1110
000000	0	0011	011100	0	1110
000000	0	0100	010000	0	1110
000000	0	0101	010111	0	0001
000000	0	0110	000000	0	1110
000000	0	0111	000111	0	0111

FIG.7

INPUT			OUTPUT		
PREV_LSB	PARITY	CURR_MSB	PREV_LSB	PARITY	CURR_MSB
001100	1	100110	001110	0	111010
100110	0	110011	100111	0	111011
110011	0	011001	110111	0	111001
011001	1	001100	010111	0	011100

FIG.8

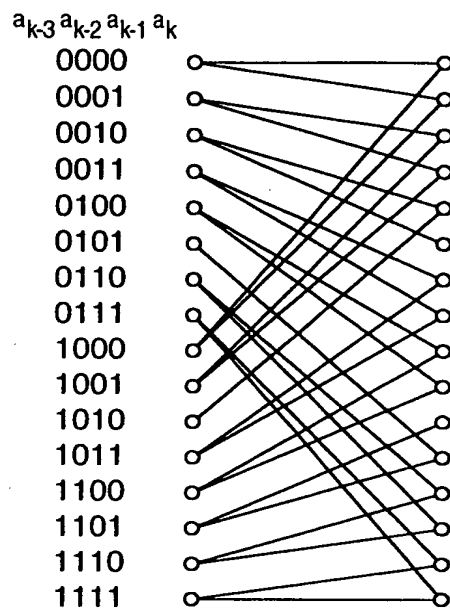


FIG.9

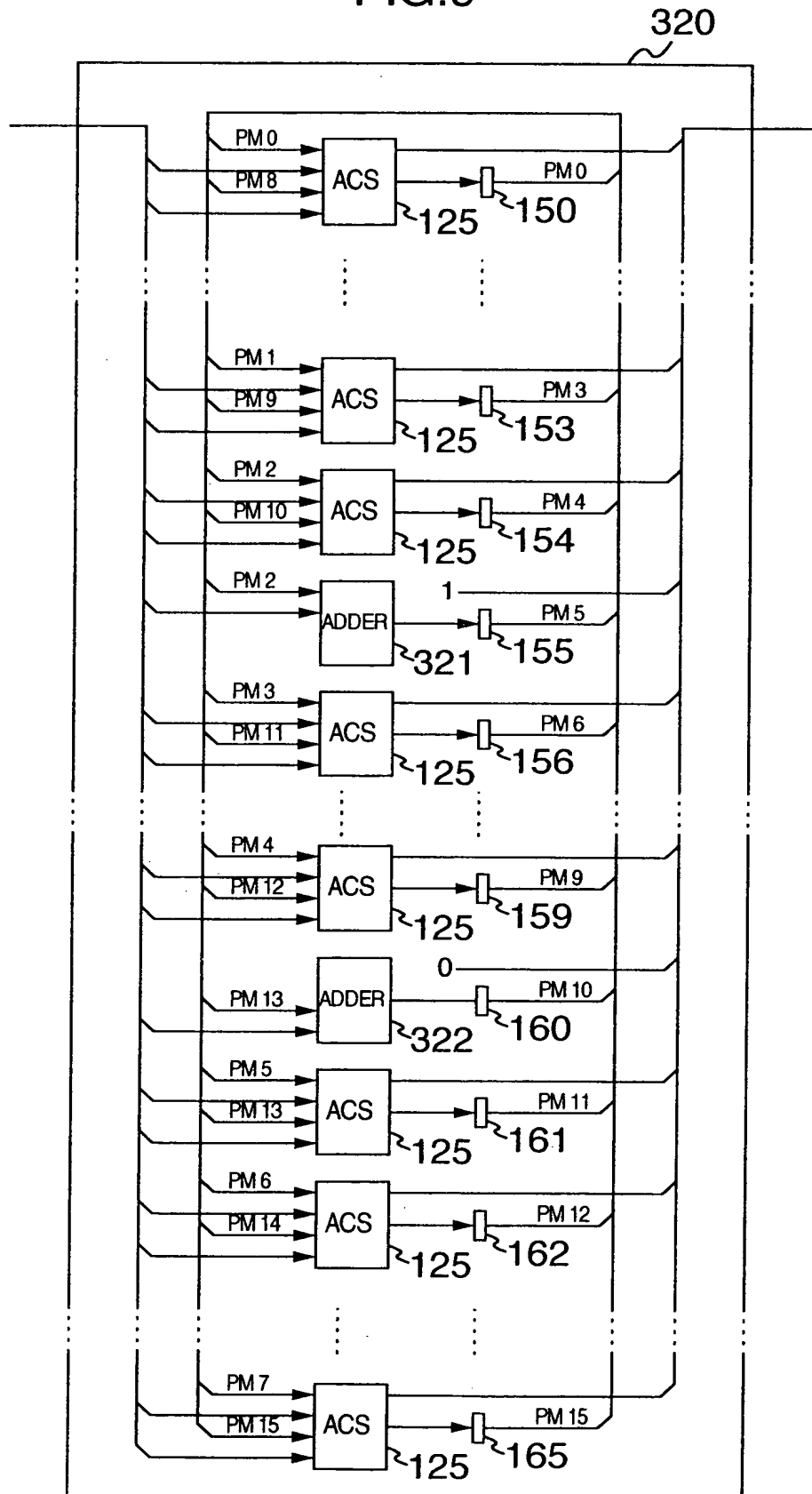


FIG.10

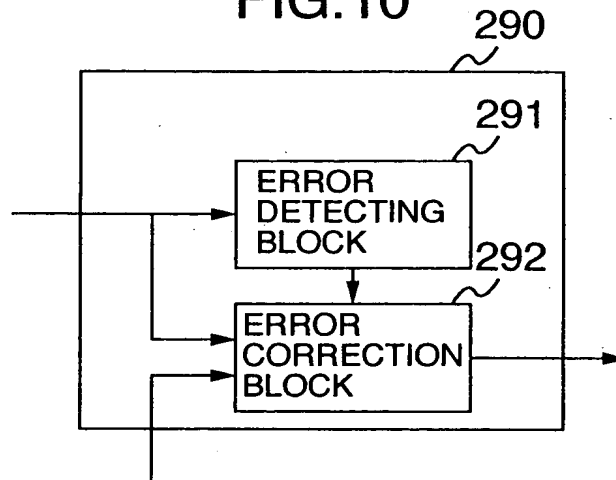


FIG.11

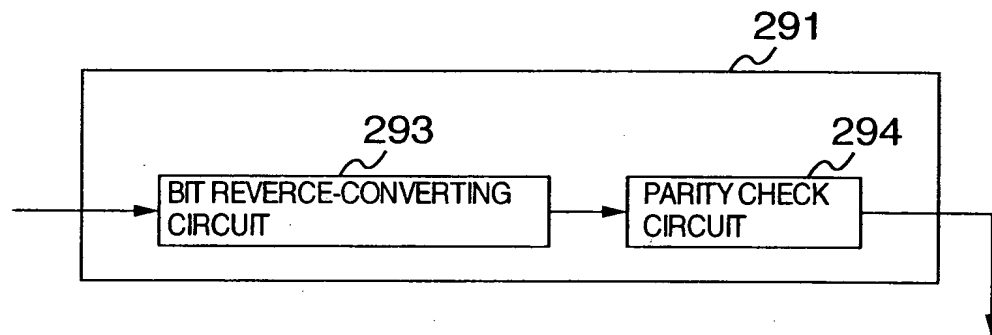


FIG.12

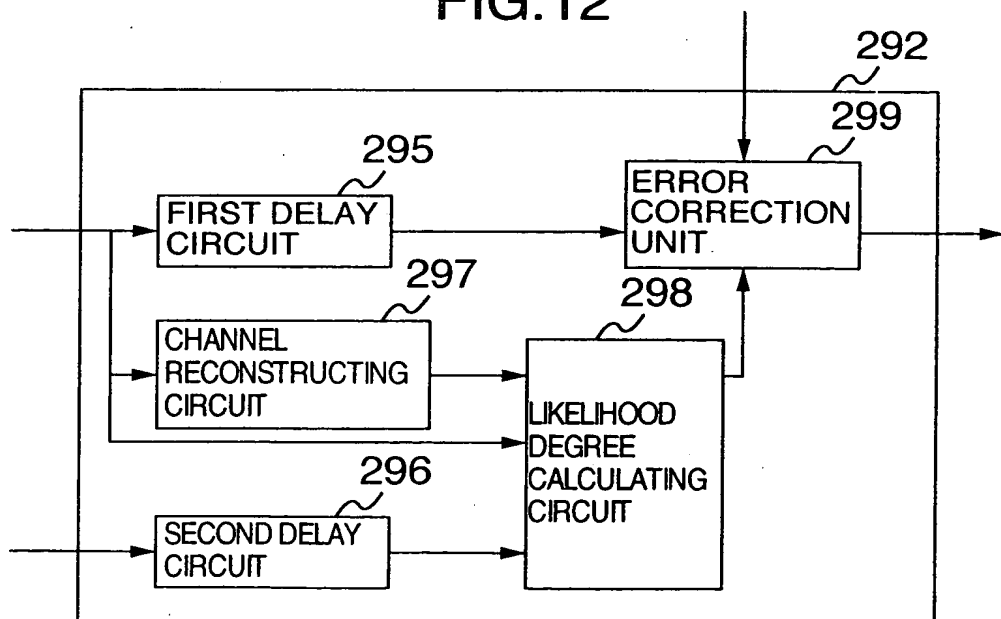


FIG.13

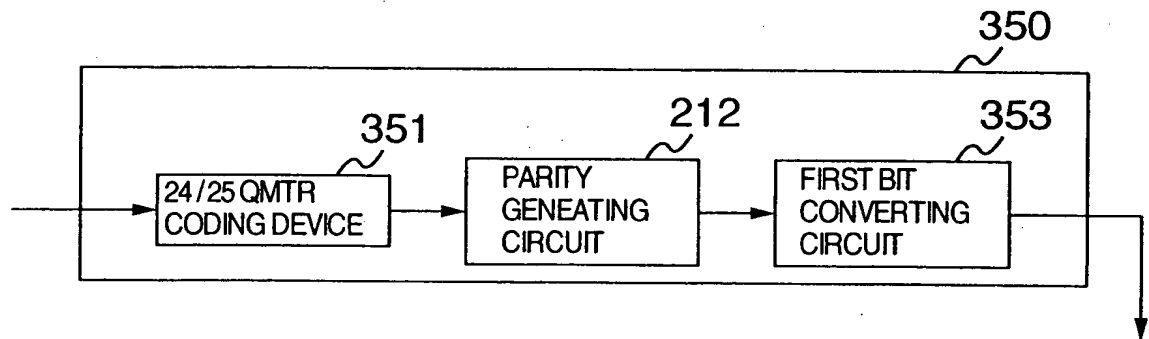


FIG.14

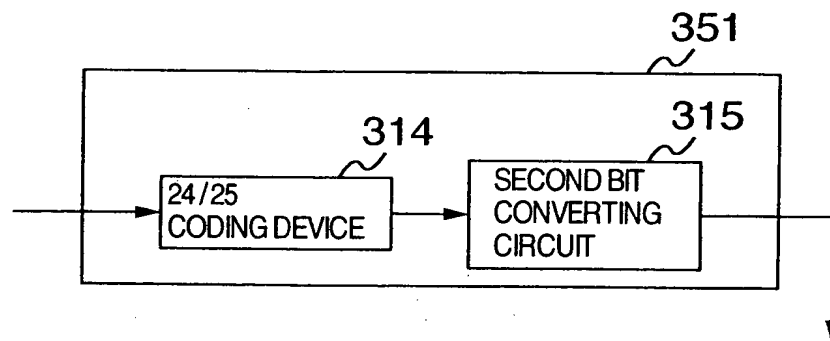


FIG.15

INPUT		OUTPUT	
PREV_LSB	CURR_MSB	PREV_LSB	CURR_MSB
0011	0011	0111	0001
1001	1001	0111	0010
1100	1100	1000	1110
0110	0110	0100	1110

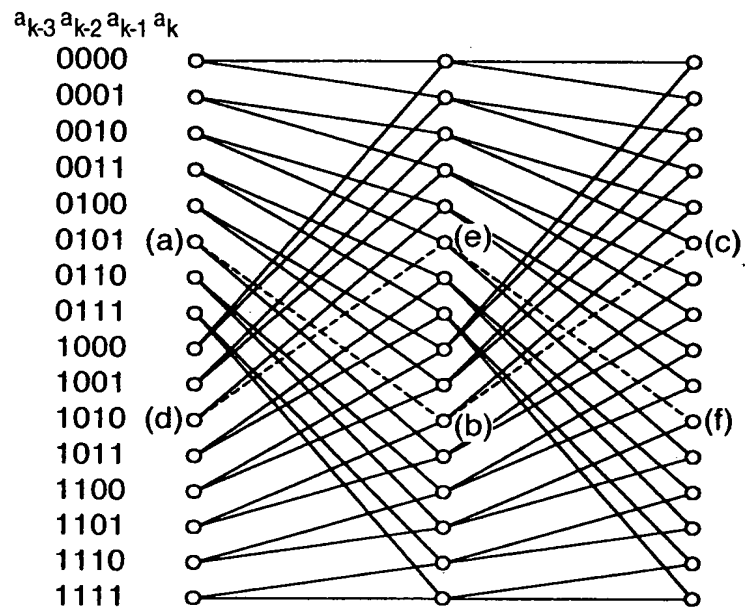
FIG.16

INPUT			OUTPUT		
PREV_LSB	PARITY	CURR_MSB	PREV_LSB	PARITY	CURR_MSB
0011	1	1100	0111	0	1110
0011	1	1101	0111	0	1101
1011	1	1100	1011	0	1110
1011	1	1101	1000	0	1110

FIG.17

INPUT			OUTPUT		
PREV_LSB	PARITY	CURR_MSB	PREV_LSB	PARITY	CURR_MSB
0011	0	0110	0111	0	0110
1001	1	0011	1001	0	1110
1100	1	1001	0111	0	1001
0110	0	1100	0110	0	1110

FIG.18



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